

**AMENDMENTS TO THE SPECIFICATION:**

Please amend the specification as follows:

[0018] FIGURE 2 illustrates an embodiment of a method of operating an SRAM device carried out according to the principals principles of the present invention.

[0023] The column peripheral circuitry 130 controls selecting the columns of the SRAM array 110 for reading and writing. The column peripheral circuitry 130 may include, for example, pre-charge circuitry, written circuitry, a column multiplexer and a sense amplifier. Additionally, the column peripheral circuitry may include address decoders for determining memory cell locations within the SRAM array 110 and control circuitry for determining between writing or reading the data. The pre-charge circuitry, the write circuitry, the column multiplexer and the sense amplifier may facilitate reading and writing data to the correct column address that has been decoded. As with the row peripheral circuitry [[130]] 120, the column peripheral circuitry 130 may also include additional components that facilitate writing and reading the data that are not illustrated or discussed herein.